# Panasonic Service and Technology Company

#### **Technical Guide**

TC-32LX70/700 LCD Display Television



**National Training** 

### Panasonic Service and Technology Company

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### **Topics**

- 1. Models Comparison
- 2. Boards Layout
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- 4. Troubleshooting
- 5. SOS Circuit
- 6. Video/Audio Processing Circuit
- 7. Panel Drive Circuit
- 8. Service Man Mode
- 9. Self-Check
- 10.Reset
- 11.Adjustments

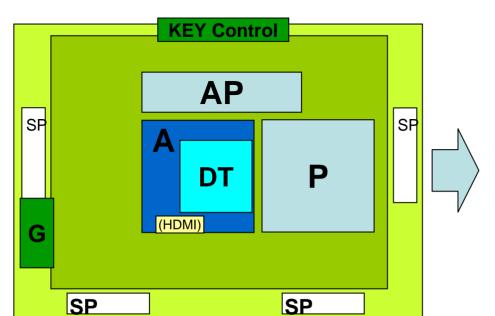
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# 2007 LCD TV Specifications

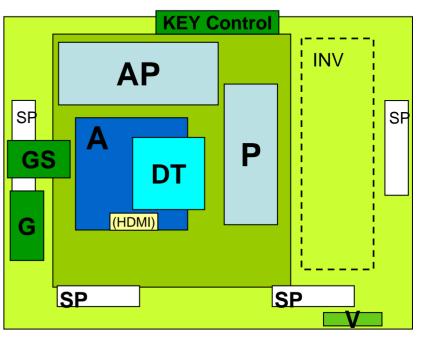
	TC-32LX700	TC-32LX70			
VIDEO					
Screen Size Diagonal (inches)	32	32			
Aspect Ratio	16:9	16:9			
Number of Pixels	1,049,088 (1,366 x 768)	1,049,088 (1,366 x 768)			
Brightness (Panel Luminance)	500 cd/m2	500 cd/m2			
Display Capability (1080p, 1080i, 720p, 480P)	Y	Υ			
Motion Picture Pro	Y	<del>_</del>			
AUDIO					
Speakers	Slim squawker x 2, woofer x 2 (L, R)	Full-range x 2 (L, R)			
Audio Output	20 W (10% THD)	20 W (10% THD)			
JACKS					
Integrated ATSC Tuner	Y	Υ			
SD Memory Card Slot	Y (SDHC Compatible)	<u> </u>			
HDMI Input	2	2			
Composite Video Input	3 (1 side)	3 (1 side)			
S-Video Input	2	2			
Component Video Input (Y, P <sub>B</sub> , P <sub>R</sub> )	1	. 1			
Monitor Output	1	1			
Digital Audio Output	1	1			
Headphone Jack	1	1			

### P.C.B Layout (LX700)

#### 32/26LX600



#### 32LX700



#### **Differences from LX600:**

GS board for SD card slot is added.

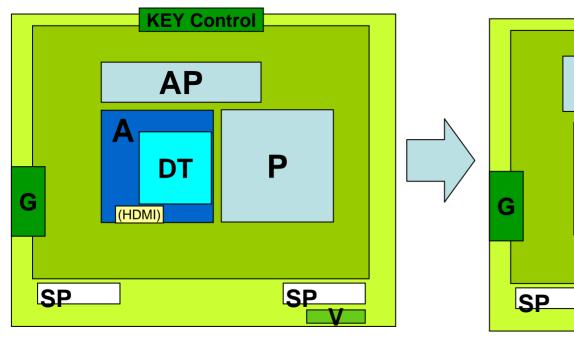
The inverter PCB of the LX700 is visible.

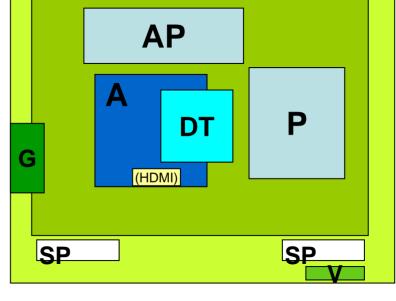
### P.C.B Layout (LX70)

#### 32/26LX60







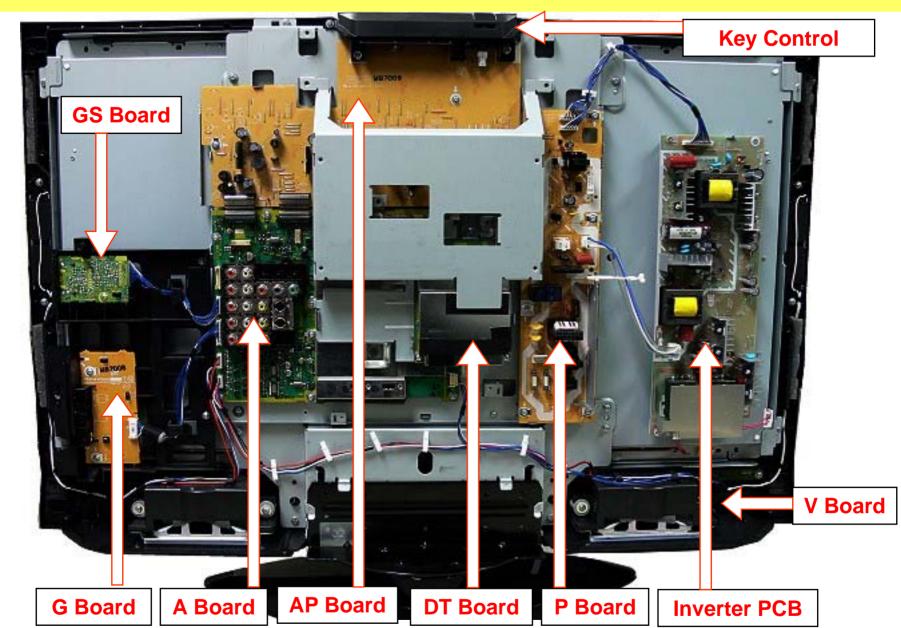


**KEY Control** 

#### **Differences from LX60:**

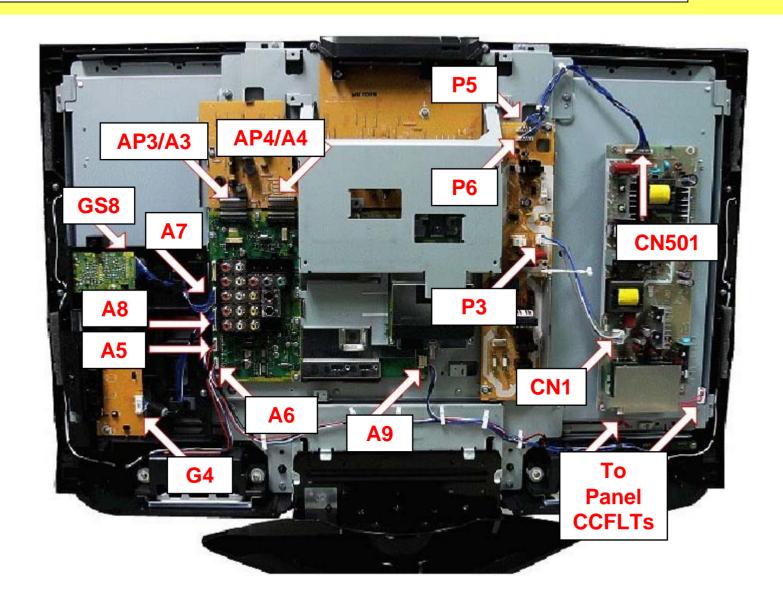
Board structure is almost same.

### P.C.B Layout (LX700 Pictorial)



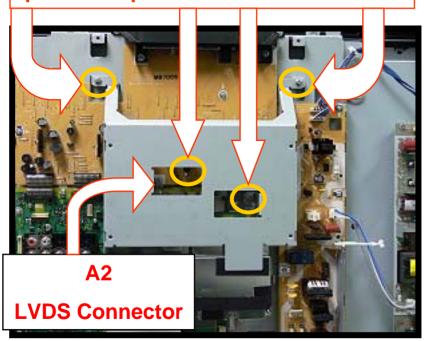
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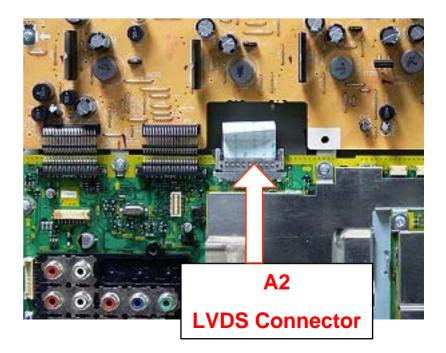
### P.C.B Layout (LX700 Connectors)



### P.C.B Layout (LX700 LVDS Connector)

Remove the 4 Screws and the metal plate to expose the LVDS Connector.

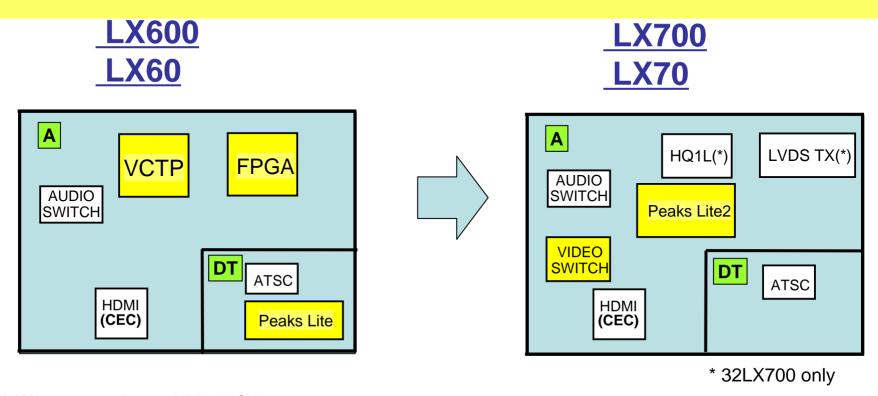




# TC-32LX700 Board Description

Board	Description	Part Number
A-Board	Main (Rear Terminal, AV Switch, MCU, Audio & Video Processor, LVDS) (Exchange Only)	TNPH0683S
AP-Board	DC-DC, Power Switch	TNPA4155ABS
DT-Board	Digital Tuner (Exchange Only)	TNAG172S
G-Board	Video 3, Headphone Jack	TNPA4266S
GS-Board	SD Card Slot	TNPA4206S
V-Board	Remote Receiver, LED	TNPA4152ADS
P-Board	Power (AC/DC) (Exchange Only)	TZRXN010MRR
Control Panel ASSY	Control Button (Non-Serviceable, Control Panel Assembly should be exchanged for service.)	K0RB00600004

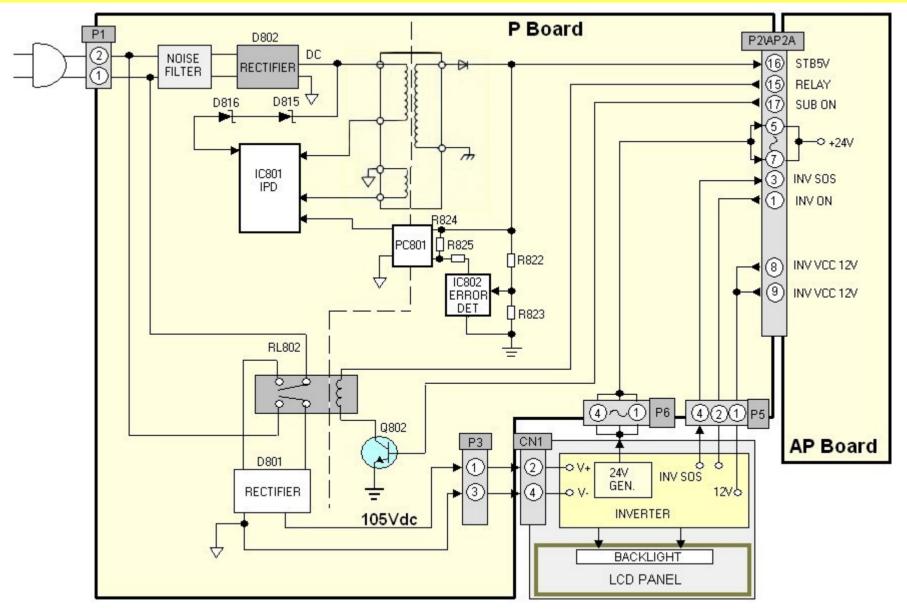
#### Differences from Current models



#### **Differences from LX600/60:**

- 1. Peaks Lite2 and VIDEO SW are introduced instead of VCTP, FPGA and Peaks Lite.
- 2. For LX700, HQ1L and LVDS TX are introduced for Motion Picture Pro.
- 3. HQ1L and LVDS TX are used to convert the picture frames from 60 to 120. This process helps to reduce after-images.
- 4. The DT board of the 32LX700 provides Channel Data output only. The MPEG2 and AC3 (Peaks Lite2) decoders are now part of the A board.

#### P Board - Main Power Board



#### P Board – Main Power Board

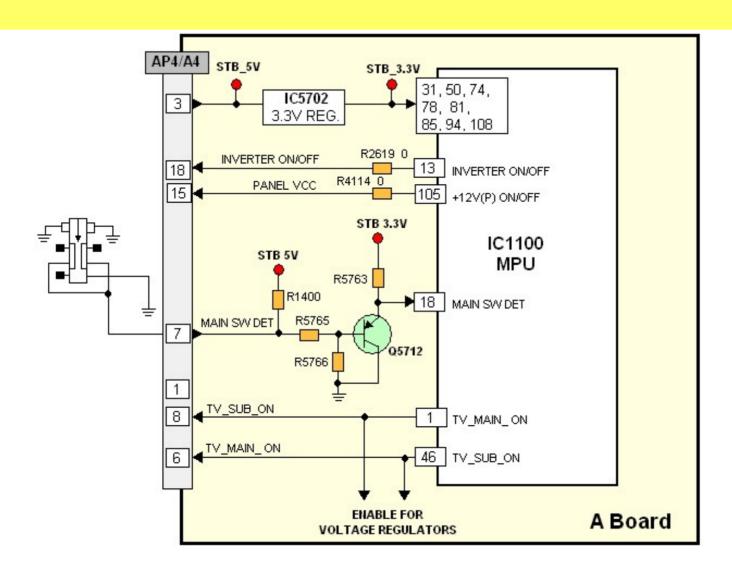
When AC is applied to the TV, the Standby circuit of the P board outputs 5Vdc to the AP board. The STB5V passes through the AP board and enters the A board. On the A board The STB5V is converted to 3.3V to power the system control circuit for standby operation. The STB3.3V is also applied to the Remote Control receiver and the power LED on the V board. The MPU (IC1100) of the A board outputs the SUB\_ON command to provide a ground connection to relay RL802 of the power supply board. On the AP board, The SUB\_ON command is used to turn on a circuit (not shown in the diagram) that provides STB5V to relay RL802 via pin 15 of connector AP2/P2. When the relay is closed, DC voltage from the rectifier, approximately 105Vdc, is supplied to the LCD panel for conversion into 24Vdc. The output of the 24V generator passes through the P board and enters the AP board for conversion into SUB\_5V, DTV9V (SUB\_9V), and HQ3.3V.

If the power button (main unit or remote control) is not pressed within 25 seconds after applying AC to the unit, the SUB\_ON command goes low to disengage the power relay. When the power button is pressed, the SUB\_ON command goes high to repeat the same operation. The voltages remain present until the unit is turned off.

When the unit is turned on, the LCD panel's inverter circuit uses the 24Vdc source to generate the 120Vac to illuminate the CCFLTs. The INV\_ON command and the 12Vdc source enter the panel's drive circuit via the connector AP2/P2 and P5 of the P board. The inverter circuit is monitored for abnormality. When a defect is detected, a SOS (High) output is provided to the A board MPU (IC1100) via Connectors P5 and P2/AP2.

Caution: The P board is not to be repaired by you. It is to be exchanged only.

### Main CPU (Power On Commands)

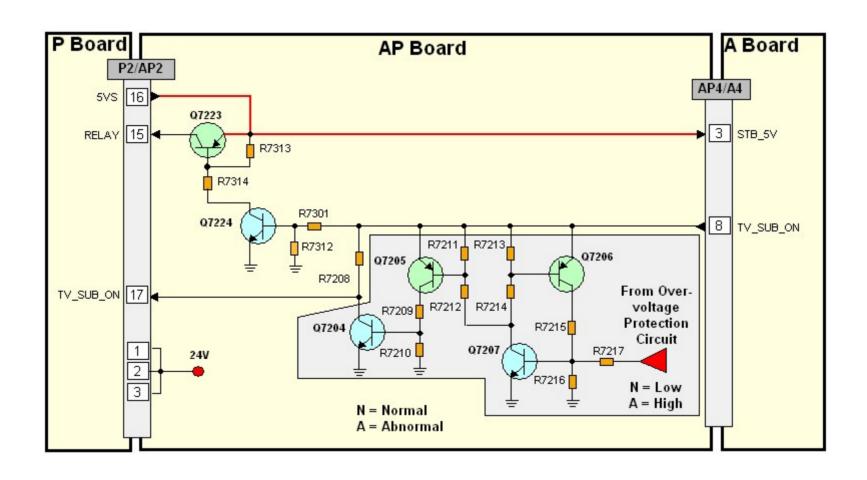


### Main CPU (Power On Commands)

The STB 5V output of the P board passes through the AP board and enters the A board when the unit is plugged into the AC outlet. IC5702 of the A board converts the STB 5V to 3.3V to power the system control circuit. Activation of the power switch causes transistor Q5712 to turn off and output STB 3.3V to pin 18 of the MPU. Upon receiving the STB 3.3V, the MPU (IC1100) outputs the TV-SUB-ON command via pin 8 of connector A4/AP4. The TV-SUB\_ON command is routed to the P board to provide a ground connection for relay RL802 of the power supply board. On the AP board, The TV\_SUB\_ON command is used to turn on a circuit (not shown un the diagram) on the A board that provides STB\_5V to relay RL802. As a result, the switch of relay RL802 on the P board closes to allow the output of 105Vdc to the LCD panel. The TV\_Main\_ON signal issued by IC1100 appears on pin 6 of connector AP4/A4 to enable the output of the 5V, 9V, and 15V DC/DC converters. Pin 105 of IC1100 outputs the PANEL VCC command to the AP board to turn on the 12V DC/DC converter. The 12V output is provided to the panel via the P board.

Caution: Measure the 105Vdc source between pin 1 and 3 of connector P3. Hot ground must be used.

### **Standby Operation**



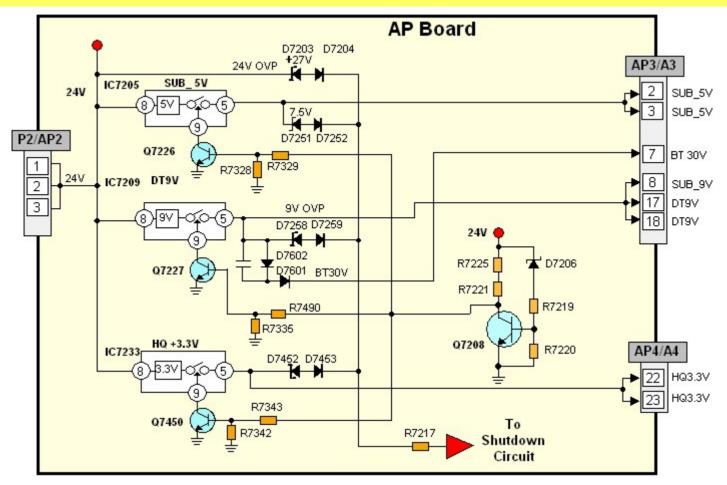
### **Standby Operation**

When the main power switch is activated, it is sensed on the A board by the MPU IC1100. The TV\_SUB\_ON signal is subsequently issued in response on pin 8 of connector A4/AP4. It turns on the transistors Q7224 and Q7223 to output the "Relay voltage on pin 15 of connector AP2/P2. The TV Sub\_On signal is also provided to the relay via the resistor R7208 and pin 17 of connector AP2/P2. This pulls the contacts of the AC relay on the P board. The AC input is rectified and provided to the LCD panel for conversion into 24Vdc. The 24V is then provided to the inverter circuit of the LCD panel to produce the 120Vac that powers the backlight CCFLTs. The same 24V passes through the P board and enters the AP board for conversion into other voltages.

The over-voltage protection circuitry affiliated with each of the regulators located on the AP board monitors for excessive current and over-voltage conditions. It is designed to turn off the AC power by disabling the SUB\_ON command that turns on the AC relay.

When an over-voltage condition is detected on the AP board, the voltage at the base of transistor Q7207 goes "high" to turn on Q7207, Q7206, Q7205, and Q7204. The TV\_SUB\_ON voltage at pin 17 of connector A2/AP2 is now grounded through Q7204 and the unit shuts down. To turn the unit back on, disconnect and reconnect the AC cord before pressing the power button.

### SUB Voltages and Over-voltage Protection

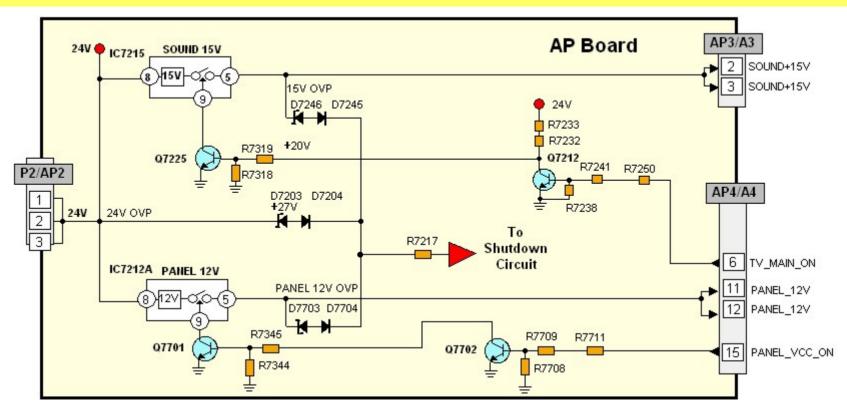


The AP Board is responsible for the regulation, distribution and dissemination of all secondary voltages derived from the 24V output of the P Board. During standby operation, the 24V output of the P board biases the transistor Q7208 into operation, causing its collector to become "low". The low at the collector of Q7208 causes the transistors Q7226, Q7227, and Q7450 to turn off. As a result, IC7205, IC7209, and IC7233 go into operation to immediately provide the Sub 5V, DT9V, BT30V, and HQ 3.3V output.

The over-voltage protection circuitry affiliated with each of the regulators monitors for excessive current and overvoltage conditions. It is designed to turn off the AC power by disabling the SUB ON signal needed to turn on the AC relay. 21

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### Main Voltages and Over-voltage Protection

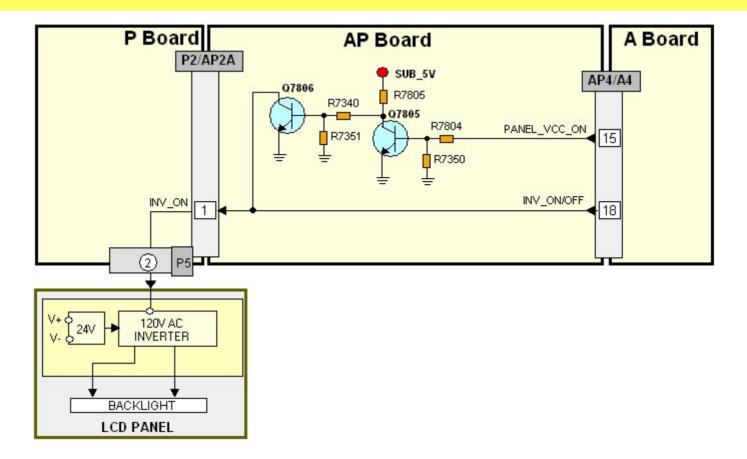


When the unit is turned on, the TV\_Main\_On command (high) of IC1100 biases the transistor Q7212 into operation, causing its collector to go "low". The low at the collector of Q7212 causes the transistor Q7225 to turn off. As a result, IC7215 goes into operation to provide the Sound\_15V output.

When the Panel\_VCC\_On command is issued, transistor Q7702 turns on causing Q7701 to turn off. Consequently, IC7212 turns on and outputs 12V. This voltage enters the A board via the connector AP4/A4. It is also provided to the LCD panel via the connector AP2/A2, the P board, and the connector P5 (not shown in the diagram).

The over-voltage protection circuitry affiliated with each of the regulators monitors for excessive current and over-voltage conditions. It is designed to turn off the AC power by disabling the SUB\_ON signal needed to turn on the AC relay.

#### Inverter On Command and Protection

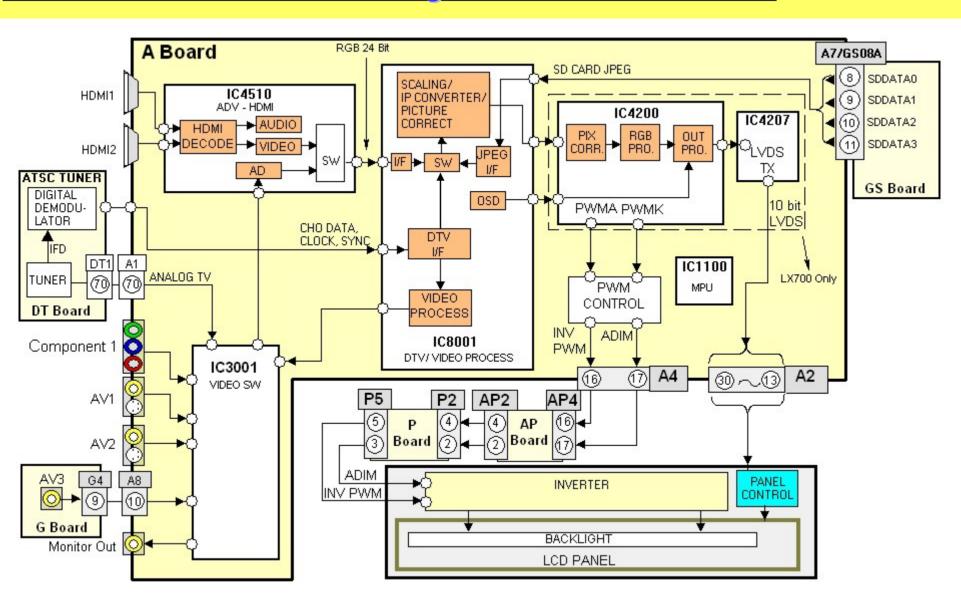


The INV\_ON/OFF command (high) of the MPU, IC1100 of the A board, passes through the AP and P boards before entering the LCD panel. On the LCD panel, it triggers the operation of the inverter to produce the 120V AC that powers the backlight CCFLTs.

To avoid catastrophic failures, when the Panel\_VCC\_ON pin of the MPU goes "low", Q7805 turns off. Consequently, Q7806 turns on to disable the operation of the inverter.

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### Video Process Block Diagram



### Video Process Block Diagram

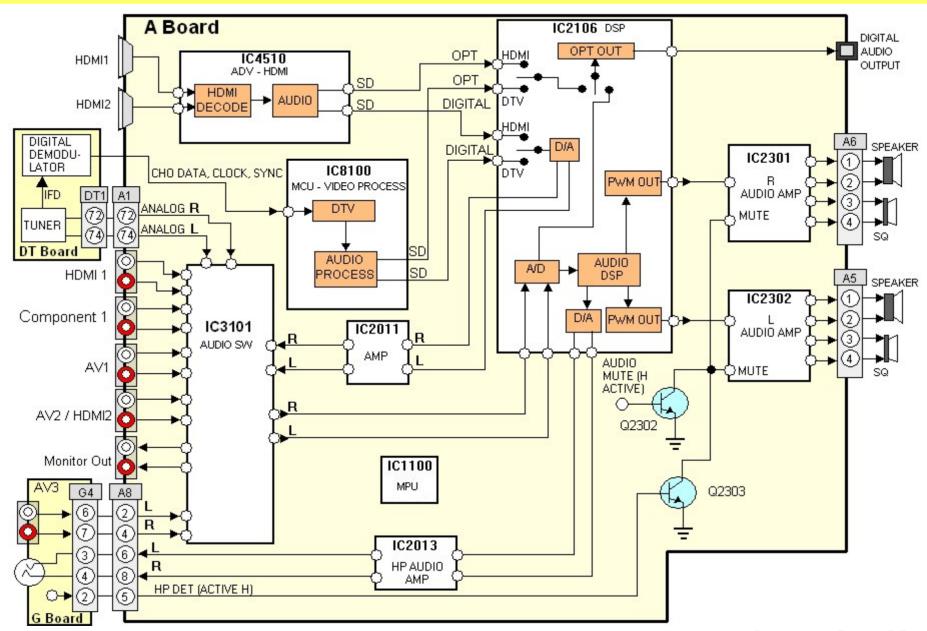
The main function of the A board is to select and process one of the incoming video signals. Video inputs 1 2, and 3, Component Video Input, and the composite video output of the tuner are all are connected to IC3001 for selection. The video output signal of the switch can be in any of the three formats: Video, Y/C, or Y, Pb, Pr. The selected output enters IC4510, the HDMI IF Receiver/Decoder IC, for A/D conversion. The comb filter inside IC4510 converts the composite video signal of the main picture to Y and C (luminance and chrominance) signals. S-Video, which is already Y/C separated, simply passes through the comb filter. The chrominance data is then applied to the Chroma Demodulator circuit to separate the color signal into Pb and Pr data. At the completion of this process, the format of the composite or S-Video signal is now the same as a digital 480i component signal. If the incoming video is in the 480p, 720P, or 1080i format, the Y, Pb, and Pr signals undergo A/D (analog to digital) conversion only. The 10 bit YUV data is provided to a video switch. The HDMI receiver section of IC4510 converts the incoming HDMI signals to a YUV video signal. The Video interface circuit selects between the two HDMI sources and outputs the YUV signal to the switch. The output of the switch is provided to the PEAKS LITE IC, IC8001.

Digital television reception of the tuner is output in the form of a transport stream (CHODATA). The transport stream enters the DTV I/F (Interface) section of IC8001 where the video signal is extracted and converted to YUV data. The output is provided to the Video Input switch for selection. The JPEG data of the GS board enters the JPEG I/F section of IC8001 for conversion into YUV data and output to the video switch. The video switch outputs the selected picture data to the Video Process circuit.

The Video Process section of the IC performs all picture control operations such as brightness, contrast, color, tint, etc. The output signal is then applied to the HQ1L circuit (IC4200) for frame doubling operation. IC4200 is the circuit responsible for the Motion Picture Pro operation that reduces afterimage by 50%. On Screen Display data such as channel numbers, Digital TV closed caption, and picture adjustments are mixed with the video data. The output of IC4200 is provided to the LVDS transmitter (IC4207). The transmitter distributes signals with low-jitter, while creating little noise. It reduces power consumption and the generated noise from data transmission. Another benefit of the LVDS standard is minimal concern for cable length.

The main MCU (IC1100) control the entire operation of the A board. It serves as the controller that monitors all operations of the TV section (not display) of the unit. When watching digital television or viewing pictures from an SD card, the video process section of IC8001 converts the digital video into analog composite video signal for output at the monitor out jack.

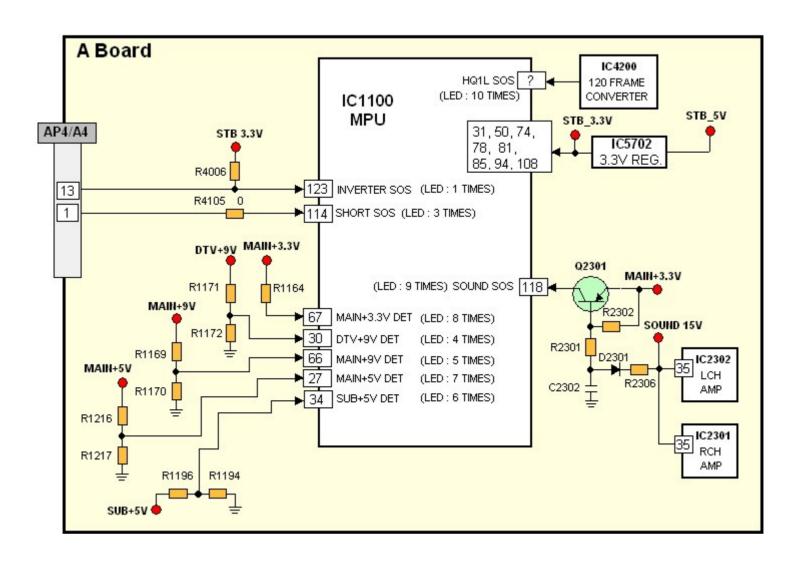
### **Audio Process Block Diagram**



#### **Audio Process Block Diagram**

IC3101 of the A board handles the audio selection of most inputs. Analog audio of the tuner and audio signals from composite, component, and DVI inputs are connected to IC3101 for selection. The output of IC3101 enters IC2106 and undergoes analog to digital conversion. The serial data output is provided to the audio DSP (Digital Signal Processor). Volume, bass, treble, and balance adjustments of the audio signal are performed by this circuit. The output is two channel PWM data that are applied to IC2301 and IC2302 for amplification. The PWM output signals of the power amplifiers are output to the right and left channel speakers for sound reproduction. The HDMI receiver section of IC4510 converts the incoming HDMI audio into serial data. The Audio interface circuit selects between the two HDMI audio and outputs the SD (serial Data) to the switch located inside IC2106. Two outputs are provided, one for the digital audio output jack, the other for the speakers. Digital television reception of the tuner is output in the form of a transport stream that contains MPEG2 video and AC3 audio. The transport stream enters the DTV I/F section of IC8001 where the audio signal is extracted and converted to serial data. Two outputs are provided to IC2106. One output consists of the AC3 audio that will be provided to the digital audio output jack. The other output consists of left and right audio that will be used to drive the speakers. Speaker output of the DTV and HDMI audio is selected and converted to analog. The analog audio is then output to an audio amplifier (IC2011) and then applied to the audio selector (IC3101). The optical jack audio signal is also selected and processed inside IC2106. Q2303 is a muting transistor that applies a low to IC2106 when a headphone jack is connected to the unit. Q2302 applies a low to IC2106 when the mute function of the unit is active.

### MPU SOS Inputs and Blink Codes



#### MPU SOS Inputs and Blink Codes

**One Blink:** One blink of the power LED indicates a problem in the inverter circuit located on the LCD panel. The inverter board is not to be repaired in the field. Therefore a replacement is required.

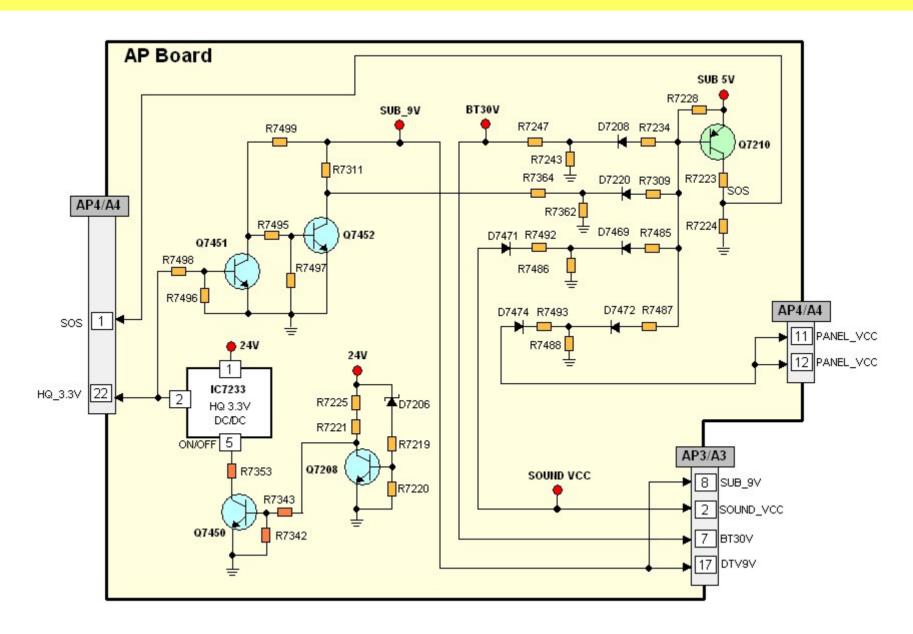
**Three Blinks:** Three blinks of the power LED indicates an over-current or over-voltage condition in one of the DC supplies of the AP board. The AP board is repairable. Therefore the use of an ohmmeter and peak-hold-voltmeter is required to determine the cause of the shutdown. An over-voltage condition is definitely a defect of the AP board. An over-current condition may be due to a short circuit located in the AP board, A board, or the LCD panel.

**Five, Seven, or Eight Blinks:** The A board is responsible for five, seven or eight blinks of the power LED. The MAIN +3.3V, MAIN +5V, and MAIN +9V sources are all produced on the A board. The detection input pins of the MPU, IC1100, monitor for their presence.

**Four or Six Blinks:** The AP board is responsible for four or six blinks of the power LED. The DTV9V, and SUB\_5V sources are produced on the AP board. The MPU IC1100 monitors for their presence when the unit is connected to AC Power. A short or open circuit on the A board may also be responsible for their absence.

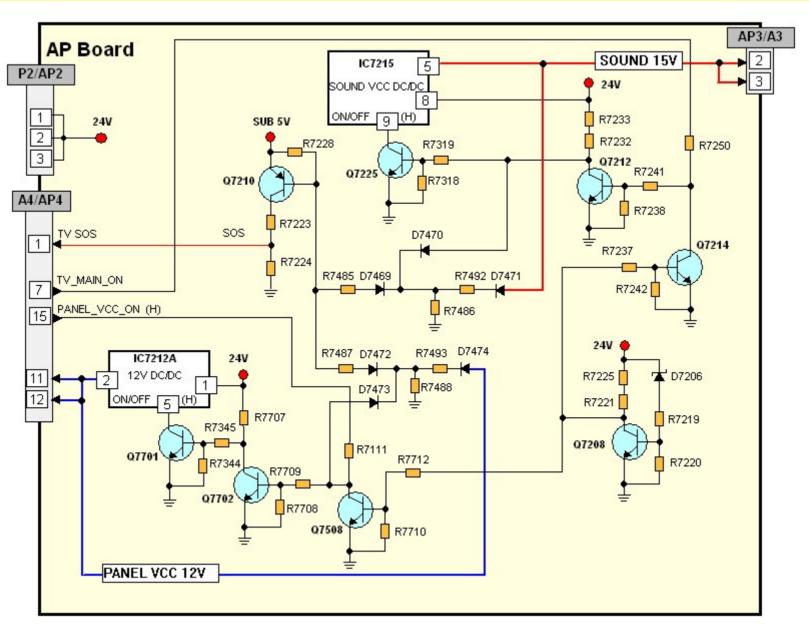
**Nine Blinks:** Nine blinks of the power LED is due a short circuit in the audio output circuit (s) of the unit. The audio output circuit is located on the A board. Confirm the status of the speakers. If defective, perform a replacement. If ok, replace the A board.

**Ten Blinks:** Ten blinks of the power LED indicates a malfunction of IC4200, the frame converter. This IC resides on the A board. Therefore ten blinks of the power LED requires the replacement of the A board.



The protection shutdown circuit is designed to monitor for a short or loss of the SUB\_9V, SOUND\_VCC, BT30V, and the Panel VCC sources. If any of these voltage sources goes low, Q7210 turns on. As a result, the collector of Q7210 goes high to signal a shutdown condition to IC1100 on the A board. The TV\_SUB\_ON pin of IC4002 goes low to deactivate the AC relay on the P board.

During standby operation, the 24 volt output of the P board biases the transistor Q7208 into conduction, causing its collector to become "low". The low at the collector of Q7208 causes the transistors Q7450 to turn off. As a result, IC7233 goes into conduction to immediately provide the HQ 3.3V output. When the 24 drops below the reverse breakdown voltage of D7206, transistor Q7208 turns off and transistor Q7450 turns on. IC7233 ceases to operate causing Q7451 to turn off and Q7452 to turn on. Consequently, the SUV\_9V line is now grounded. Diode D7220 becomes forward biased and Q7210 outputs a high to trigger the shutdown of the unit.



#### **Sound VCC Protection**

During normal operation, the 24 volt output of the P board biases the transistor Q7208 into conduction, causing its collector to become low. The low at the collector of Q7208 causes the transistor Q7214 to turn off. When the PANEL\_MAIN\_ON command (high) is issued Q7212 turns on. Consequently Q7225 turns off, and IC7215 goes into conduction to provide the SOUND 15V output.

When the 24 drops below the reverse breakdown voltage of D7206, Q7208 turns off, Q7214 turns on to ground the PANEL\_MAIN\_ON voltage. Q7212 turns off and Q7225 turns on. IC7215 ceases to operate and the Sound\_VCC is no longer output.

During normal operation, the anode of D7469 receives 5V and the junction of D7469, D7470, R7486, and R7492 receives 12V. Due to this condition, the transistor Q7210 remains off. If the SOUND\_15V line becomes shorted, diode D7469 goes into conduction to turn Q7210 on. Consequently Q7210 outputs a high to shut down the unit.

#### **Panel VCC Protection**

During normal operation, the 24 volt output of the P board biases the transistor Q7208 into conduction, causing its collector to become low. The low at the collector of Q7208 causes the transistor Q7508 to turn off. When the PANEL\_VCC\_ON command (high) is issued, Q7702 turns on. Consequently Q7701 turns off, and IC7212 goes into conduction to provide the PANEL 12V output.

When the 24 drops below the reverse breakdown voltage of D7206, Q7208 turns off, Q7508 turns on to ground the PANEL\_VCC\_ON voltage. Q7702 turns off, and Q7701 turns on. IC7212 ceases to operate and the Panel VCC is no longer output.

During normal operation, the anode of D7472 receives 5V and the junction of D7472, D7473, R7493, and R7488 receives 12V. Due to this condition, the transistor Q7210 remains off. If the Panel\_12V line becomes shorted, diode D7472 goes into conduction to turn Q7210 on. Consequently Q7210 outputs a high to shut down the unit.

When the unit is turned on, the TV\_Main\_On command (high) of IC1100 biases the transistor Q7212 into operation, causing its collector to become "low". The low at the collector of Q7212 causes the transistors Q7225 to turn off. As a result, IC7215 goes into operation to provide the Sound\_15V output.

### **Power LED Timing Table**

Blinking Times	Blinking Timing	Contents	Check Points
1	once 3 sec	INVERTER SOS	LCD PANEL
3		BT 30V SOUND 15V PANEL 12V	AP-BOARD
4		DTV 9V	AP-BOARD
5		MAIN 9V	A-BOARD

## **Power LED Timing Table**

Blinking Times	Blinking Timing	Contents	Check Points
6		SUB 5V	AP-BOARD
7		MAIN 5V	A-BOARD
8		MAIN 3.3V	A-BOARD
9		SOUND SOS	A-BOARD AP-BOARD
10		HQ1L SOS	A-BOARD

#### Self-Check and Reset Function

- 1. Checks the communication IIC bus lines
- 2. Provides a SOS History

<u>To Access the Self-Check Mode</u>: Turn the TV on and select an active channel. While pressing the "VOLUME ( - )" button on the main unit, press the "OK" button on the remote control for more than 3 seconds.

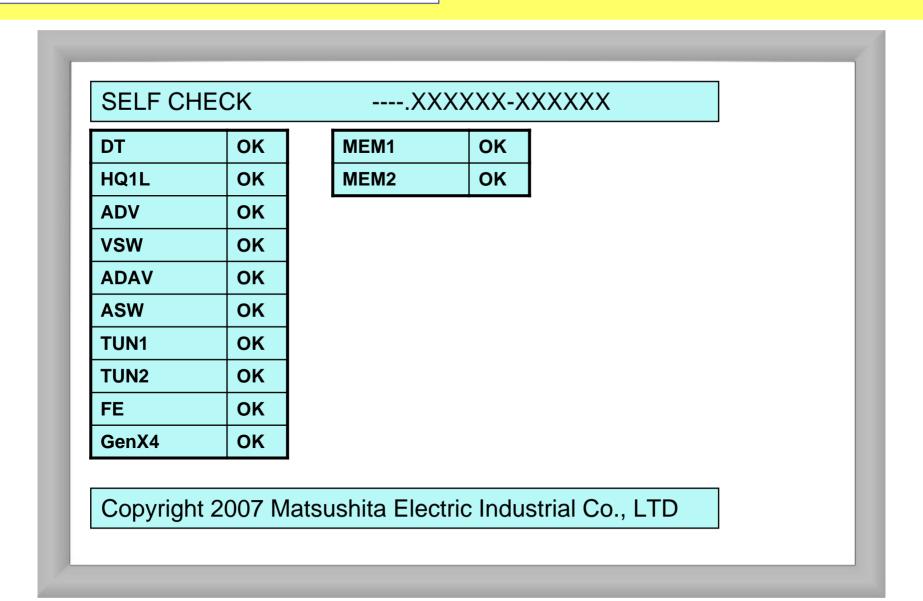
This procedure is useful to display the unit's firmware version and check IC data communications.

The Self-Check function clears channel programmed settings, picture settings, channel labels, LOCK mode settings, and password.

It restores the unit to factory shipping settings.

To Exit the Self-Check Mode, Disconnect the AC cord from the wall outlet.

#### Self-Check Menu



# **Check Point**

DISPLAY	Ref. No.	Description	P.C.B.
DT	IC8001	Peaks 2	A-Board
HQ1L	IC4200	HQ1L	A-Board
ADV	IC4510	A/D CONV. , HDMI RX	A-Board
VSW	IC3001	VIDEO SW	A-Board
ADAV	IC2106	AUDIO DSP	A-Board
ASW	IC3101	AUDIO SW	A-Board
TUN1	TU8300	TUNER (PLL block)	DT-Board
TUN2	TU8300	TUNER (MTS block)	DT-Board
FE	IC8302	FRONT END (Digital demodulator)	DT-Board
GenX4	IC1100	GenX4 (STB MCU)	A-Board
MEM1	IC1101	EEPROM (GenX4)	A-Board
MEM2	IC8503	EEPROM (Peaks)	A-Board
		20	

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#### Serviceman Mode

To enter the Serviceman Mode, turn the TV on and while pressing the "VOLUME ( - )" button on the main unit, press the "RECALL" button of the remote control three times within 3 seconds.

To exit the Serviceman Mode, Press the Power button on the TV or the Remote Control.

## Serviceman Mode Menu and Navigation

SERVICE ADJUST WB-ADJ OPTION RM-SET SVR-TOOL

Peaks Soft	0.270
Peaks EEP	1.00
LSI DATA	0.00.36
GenX SOFT	0.23.00
GenX EEP	0.40.00
GenX COR	0.00.00
HQ1L EEP	15

NOTE: This is not part of the Menu.

#### **Key command**

"1" button...Main items Selection in forward direction

"2" button...Main items Selection in reverse direction

"3" button...Sub items Selection in forward direction

"4" button...Sub items Selection in reverse direction

"VOL" button...Value of sub items change in forward direction (+), in reverse direction (-)

## **Adjustment Data**



"1" or "2" button of the remote control to select the main Adj. item

"3" or "4" button of the remote control to select the sub Adj. Item

"Vol. (-)" or" Vol. (+)" to change the data

Note: Record the original data on a sheet of paper prior to making any adjustment.

SVR-TOOL		
TD2Microcode: 81c0000e		
Flash ROM: 1- 227E		
PCTC: 00 . 00 . 00 . 00 . 00	Time 000040:40 Count 0000049	

#### To access:

- 1. Select "SRV-TOOL" in the Serviceman Mode menu.
- 2. Press the [OK] button on the remote control.

### **SRV-TOOL**

#### **Display of SOS History**

#### **SOS History**

(Number of LED blinking) indication. From the left side; Last SOS, before Last, third occurrence before last, 2<sup>nd</sup> occurrence after shipment, 1<sup>st</sup> occurrence after shipment. This indication will be cleared by entering the "Self-check mode. The unit will be forced to factory shipment setting".

#### **POWER ON TIME/COUNT**

Time: Cumulative power on time, indicated in decimal numbers\_\_ hour: minute

Count: Number of power on/off times in decimal numbers

This indication will not be cleared by self-check or any command.

#### **Exit:**

Disconnect the AC cord from wall outlet.

# Voltage chart of AP-board

### **AP-board**

VOLTAGE	TEST POINT	SPECIFICATION	CONNECTOR
24V	TP7904	24.0 ± 1.2V	AP2 _PIN 1, 2, 3
SUB_5V	TP7211	5.12 ± 0.25V	AP3_PIN 21, 22
SUB_9V	TP7213	9.27 ± 0.45V	AP3_PIN 8, 17, 18
BT_30V	TP7601	31.5 ± 2.5V	AP3_PIN 7
HQ_3.3V	TP745	3.38 ± 0.17	A-Board
SOUND_15V	TP7209	15.2V ± 0.75V	AP3_PIN 2, 3
PANEL_12V	TP7702	12.14 ± 0.6V	AP4_PIN 11, 12
STB_5V	TP7151	5.0 ± 0.5V	AP4_PIN 3

# Voltage chart of P and A boards

#### P-board

VOLTAGE	TEST POINT	SPECIFICATION
STB_5V	TP823	5.0 ± 0.5V

### **A-board**

VOLTAGE	TEST POINT	SPECIFICATION
STB3.3V	TP7006	3.3 ± 0.16V
SUB1.2V	TP5601	1.26 ± 0.06V
SUB1.8V	TP5602	1.83 ± 0.09V
SUB3.3V	TP5600	3.3 ± 0.16V
MHQ1.2V	TP4209	1.22 ± 0.06V
MHQDDR2.5V	TP4210	2.5 ± 0.12V

## Voltage chart of DT board

### A-board --- DT-board

VOLTAGE	TEST POINT A- BOARD	SPECIFICATION
<b>†</b>	A- BOARD	<u> </u>
SUB_3.3V	TP8332	3.3 ± 0.16V
SUB_1.2V	TP8328	1.26 ± 0.06V
SUB_5V	TP8326	5.12 ± 0.25V
30V_A	TP8331	31.5 ± 2.5V

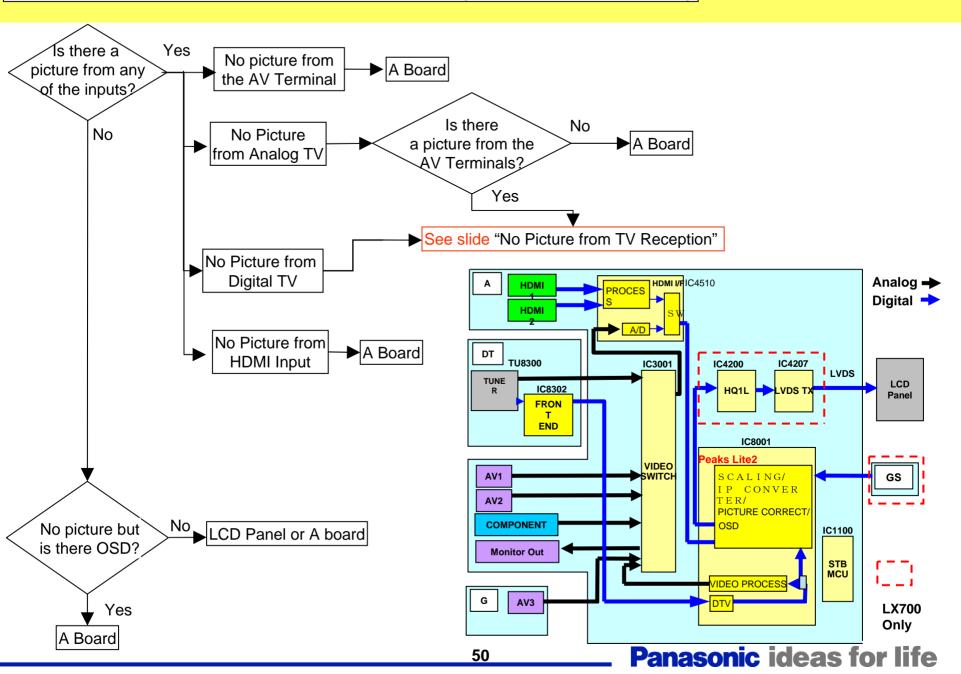
Purposely left blank

# **Troubleshooting Signal Problems**

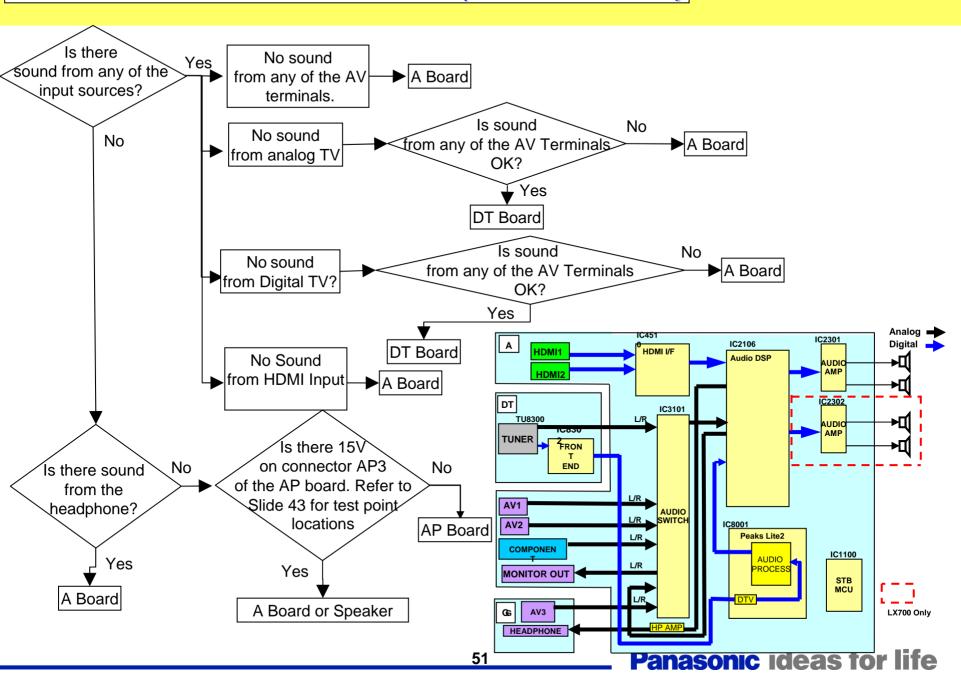
We can classify the signal problem into three categories. It is important to check all functions to determine whether both picture and sound problem exist. Checking all functions helps us determine whether the cause of the problem is the panel, the video processor or sound processor.

- 1) Picture NG and Sound OK
- 2) Picture OK and Sound NG
- 3) Picture NG and Sound NG

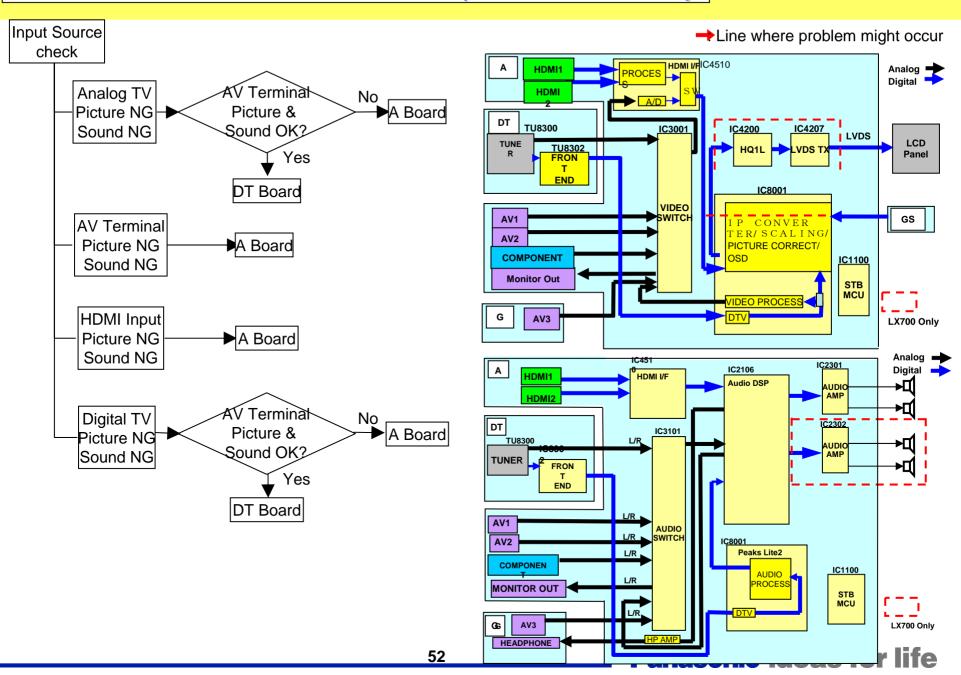
# Picture NG and Sound OK (LX700/LX70)



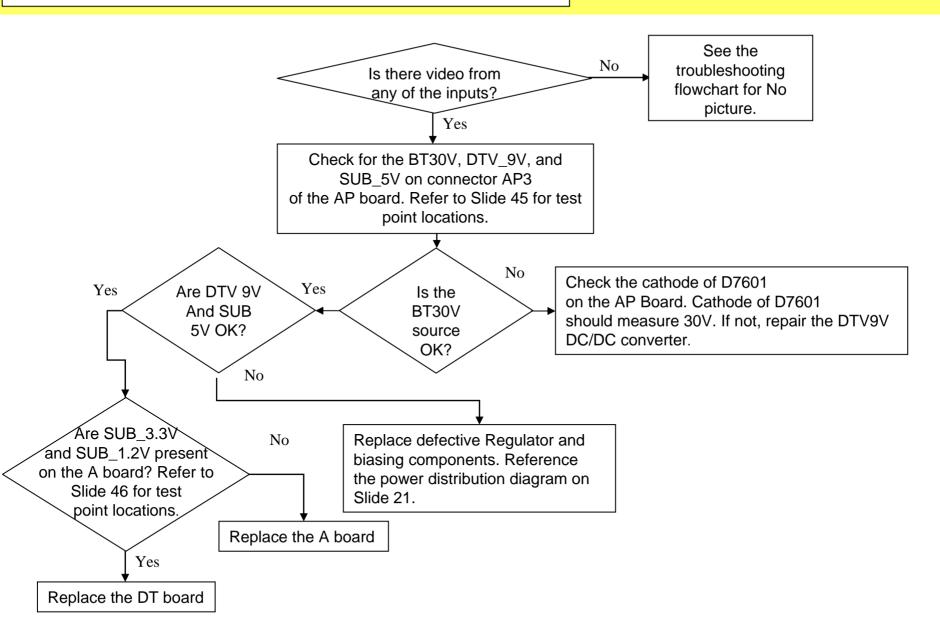
# Picture OK and Sound NG (LX700/LX70)



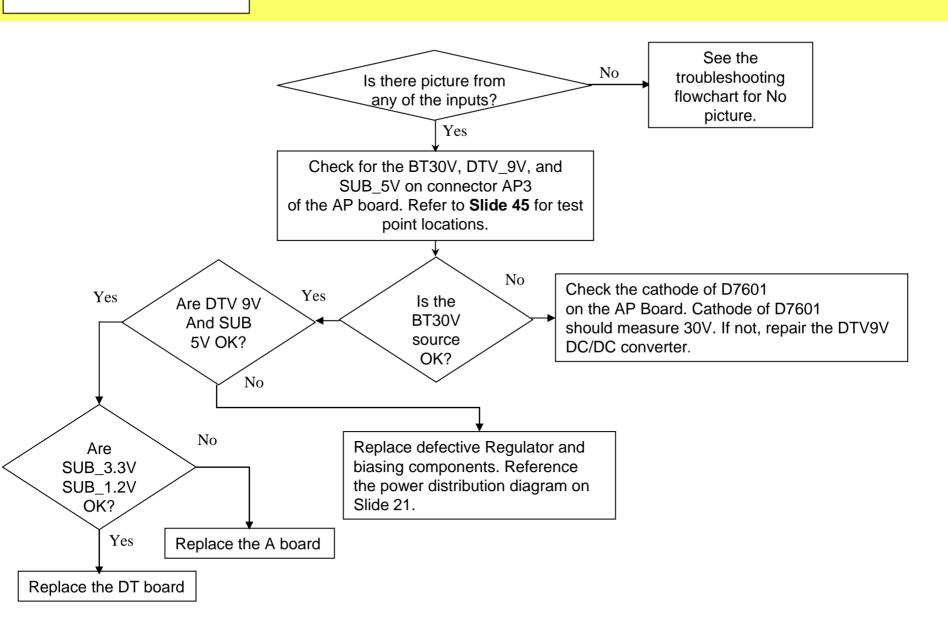
# Picture NG and Sound NG (LX700/LX70)



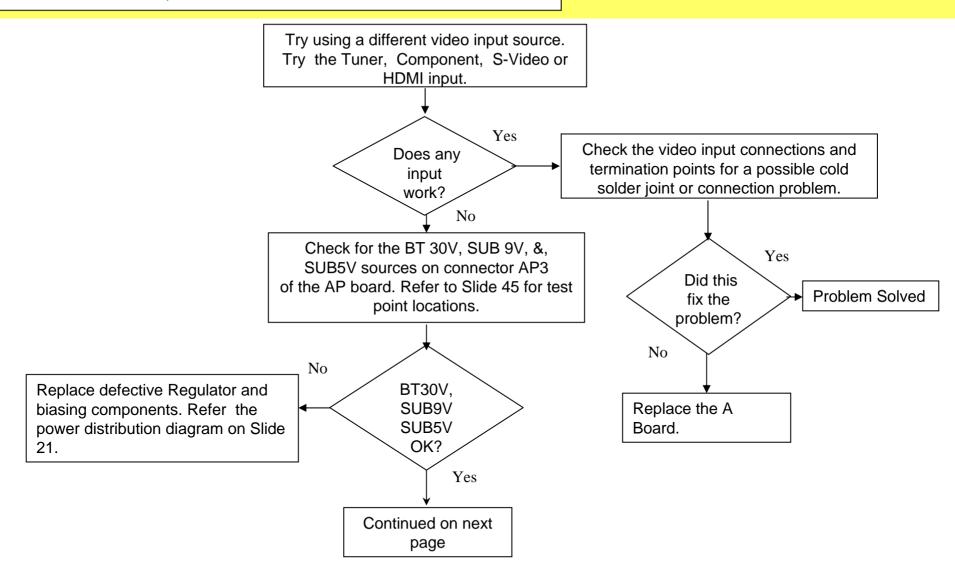
# No Picture from TV Reception



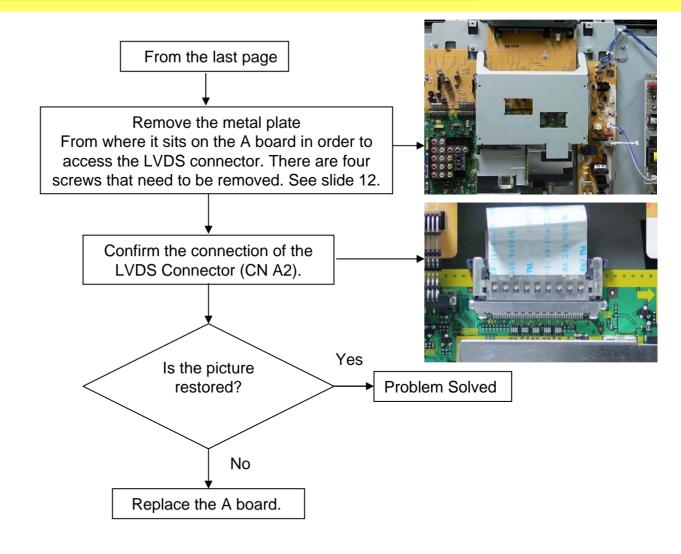
#### No Picture



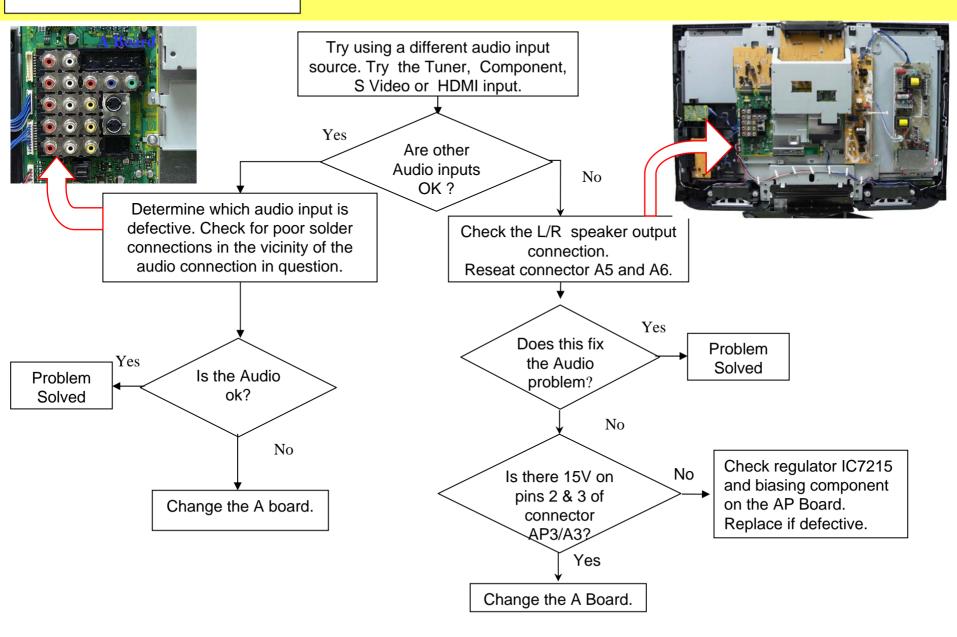
## No Video, Dark Blue Screen



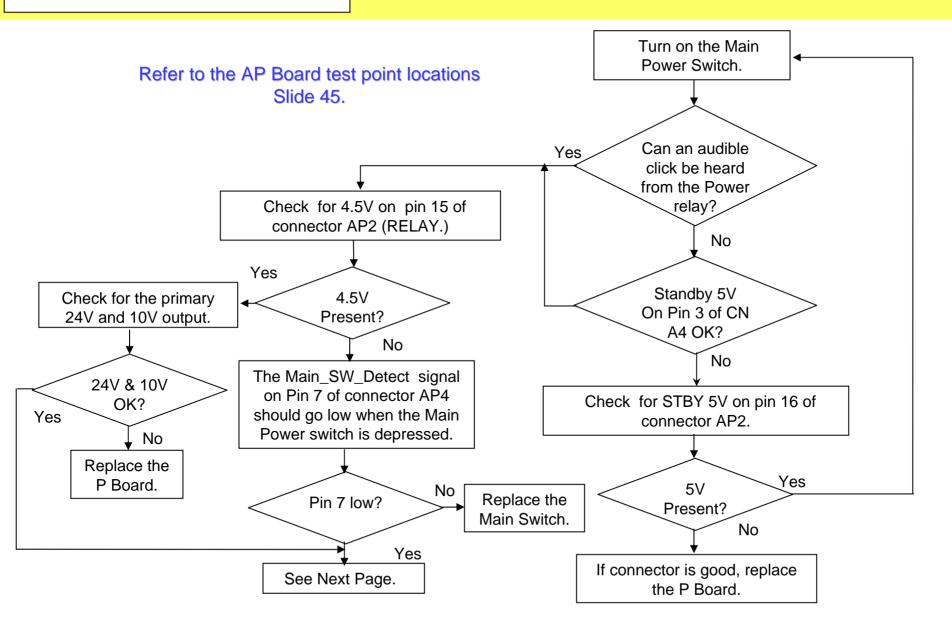
## No Video, Dark Blue Screen (Continued)



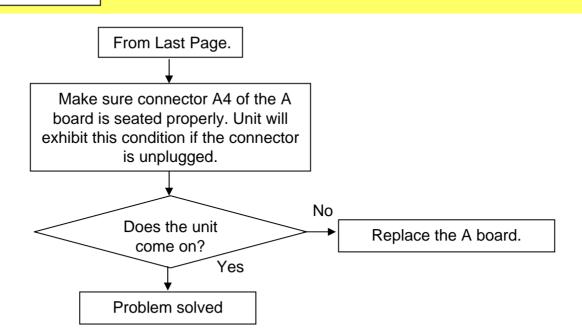
### No Audio



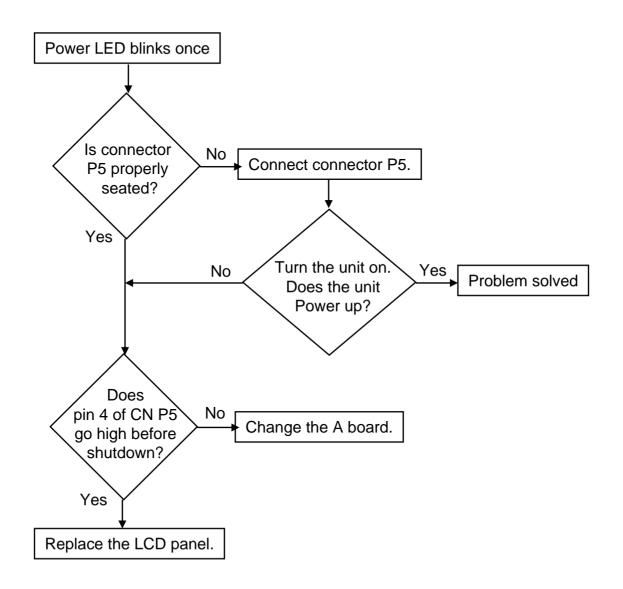
#### No Power



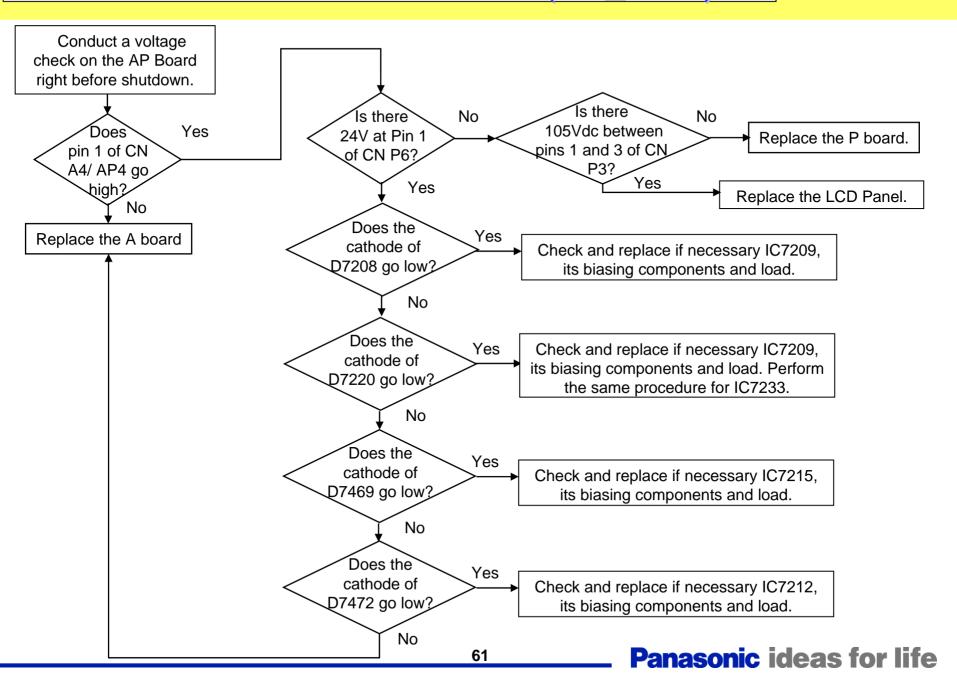
## **No Power**



# Power LED Blinks One Time (INV\_SOS)



## Power LED Blinks Three Times (TV\_SOS)



## **Power LED Blinks Four Times**

